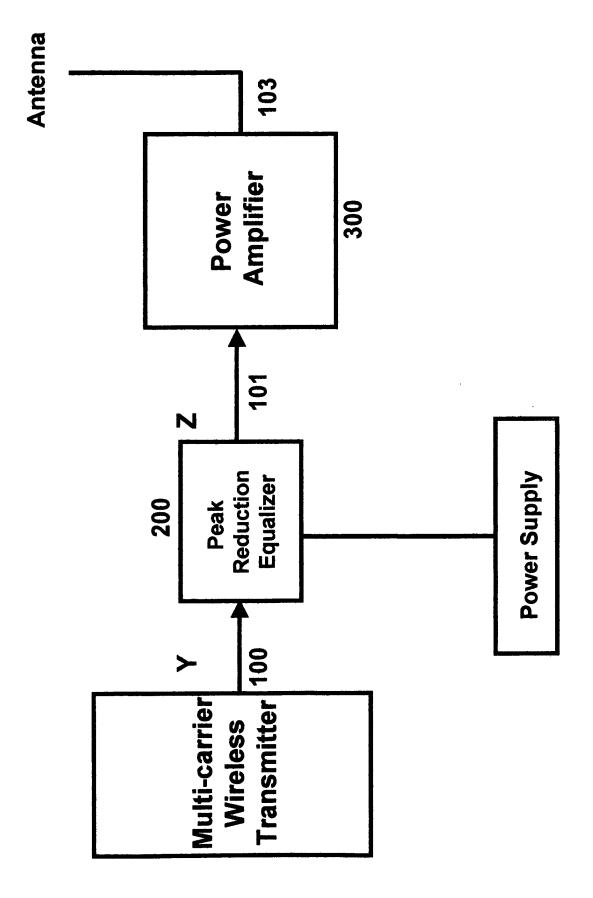


Figure 1



To PA Input 101 Generate Clock Carrier Multi 205 203 RS232 Processing Figure 2 Baseband Unit 202 400 Carrier Multi Reg Volt Rx1 201 204 100

Figure 3

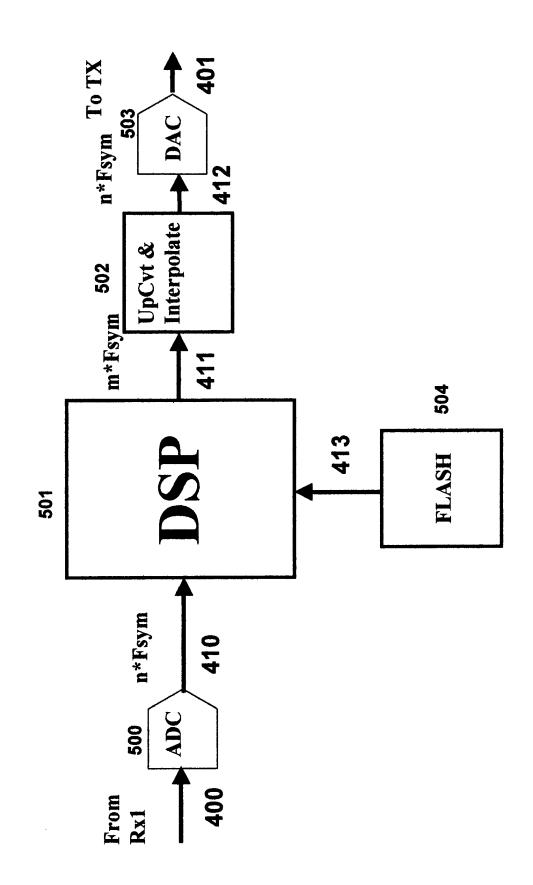
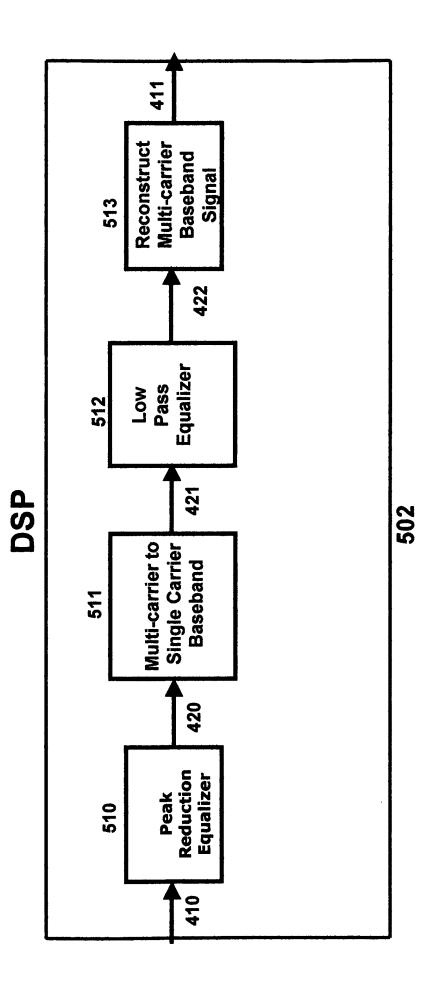


Figure 4



COMBINER 683 NCO nn NCO n NC0 2 NCO 1 Peak Reduction Equalizer 

Figure 5